## **CLAIMS**

What is claimed is:

1.	A method of passivating an integrated circuit (IC), the method
comprising:	
	forming an insulating layer on said IC;
	forming an adhesion layer on a surface of said insulating layer by
treating said	surface of said insulating layer with a gas; and,
•	forming a first passivation layer upon said adhesion layer, said first
passivation ?	layer and said gas including at least one common chemical element.
2.	The method of claim 1 further comprising forming a second
passivation ?	layer upon said first passivation layer.
3.	The method of claim 1 wherein said insulating layer includes silicon
dioxide.	
4.	The method of claim 1 wherein said gas reacts with said surface of said
insulating la	ayer.
5.	The method of claim 1, wherein said gas includes nitrous oxide (N20).
6.	The method of claim 1, wherein said gas includes one of oxygen and
nitrogen (N)	, and oxygen and ammonia (NH3), and oxygen and argon (Ar), and
ozone (O3) a	and argon.
7.	The method of claim 5, wherein said adhesion layer includes silicon
oxynitride (	Six0yNz).
	treating said  passivation  2.  passivation  3.  dioxide.  4.  insulating la  5.  6.  nitrogen (N) ozone (O3) a  7.

1	8.	The method of claim 7, wherein said first passivation layer includes
2	silicon nitric	de. \
1	9.	The method of claim 1, wherein said first passivation layer is deposited
2	upon said ac	lhesion layer by way of a process of plasma enhanced chemical vapor
3	deposition (I	PECVD).\
1	10.	The method of claim 8, wherein said at least one chemical element
2	includes niti	rogen (N).
1	11.	The method of claim 2 wherein said second passivation layer includes
2	polyimide.	
T	12.	A method of passivating an integrated circuit (IC), the method
-2	comprising:	
<b>Æ</b>		forming an oxide layer on said IC;
4:		forming an adhesion layer on a surface of said oxide layer by treating
<b>5</b> .≟	said surface	of said oxide layer with nitrous oxide gas; and
61 13 4		forming a first passivation layer of silicon nitride upon said adhesion
7	layer.	
1	13.	The method of claim 12 further comprising forming a second
2	passivation l	ayer upon said first passivation layer.
1	14.	The method of claim 12, wherein said adhesion layer includes silicon
2	oxynitride.	
1	15.	The method of claim 12, wherein said first passivation layer of silicon
2	nitride is dep	posited upon said adhesion layer by way of a process of plasma enhanced
3	chemical var	por deposition (PECVD).



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## a silicon nitride hard passivation layer formed on said silicon oxynitride adhesion layer.

- 24. The integrated circuit passivation layer of claim 23 further comprising a photodefinable polyimide soft passivation layer formed on said silicon nitride hard passivation layer.
  - 25. A method of passivating a trench on a semiconductor substrate, comprising the steps of:

forming at least one trench;

forming an insulating layer on said at least one trench;

forming an adhesion layer on a surface of said insulating layer by treating said surface of said insulating layer with a gas; and,

forming a first passivation layer upon said adhesion layer, said first passivation layer and said gas including at least one common chemical element.

- 26. The method of claim 25, wherein said gas reacts with said surface of said insulating layer.
- 27. The method of claim 25, wherein said gas includes nitrous oxide (N2O).
- 28. A method of passivating spacers, the method comprising the steps of:
- 2 forming at least one spacer;
- forming an insulating layer on said at least one spacer;
- forming an adhesion layer on a surface of said insulating layer by treating said
  surface of said insulating layer with a gas; and,
- forming a first passivation layer upon said adhesion layer, said first
  passivation layer and said gas including at least one common chemical element.

30. The method of claim 28, wherein said gas includes nitrous oxide (N2O).

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